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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Appl. No.: 10/731,298

Applicant(s): Abadecr et al.

Filed.: December 9, 2003

Art Unit: 2816

Dkt. No.: END920030103US1

Examiner: TRA, ANH QUAN

Title: **METHOD AND CIRCUIT FOR COMPENSATING FOR TUNNELING CURRENT**

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Honorable Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**REQUEST FOR RECONSIDERATION**

This Request for Reconsideration is being filed in response to the Office Action mailed  
on January 14, 2005.

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